

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Hideo KUBOTA et al.

Appln. No.:

Filed: HEREWITH

For: SEMICONDUCTOR PROCESSING DEVICE

* * *

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

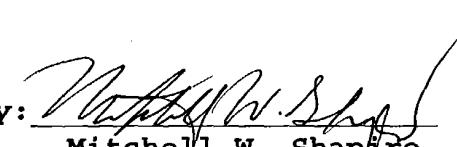
Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the document listed on the attached Form PTO-1449 is hereby cited.

The document on the attached List was cited in the specification, on pages 2 and 3, and its relevance is indicated therein.

Respectfully submitted,

MWS:skj

Miles & Stockbridge P.C.
1751 Pinnacle Drive
Suite 500
McLean, Virginia 22102-3833
(703) 903-9000

By: 
Mitchell W. Shapiro
Reg. No. 31,568

July 18, 2003

FORM PTO-1449				Atty. Docket No. XA-9882	Appn. No.		
LIST OF DOCUMENTS CITED BY APPLICANT				Applicant Hideo KUBOTA et al.			
				Filing Date HEREWITH	Group		
U. S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-class	Filing Date
	AA						
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	AH						
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FOREIGN PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Country	Class	Sub-class	Translation
	AJ	5-233844	09/10/93	Japan			Abstract
	AK						
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OTHER (including author, title, date, pertinent pages, etc.)							
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